

09751829.122900

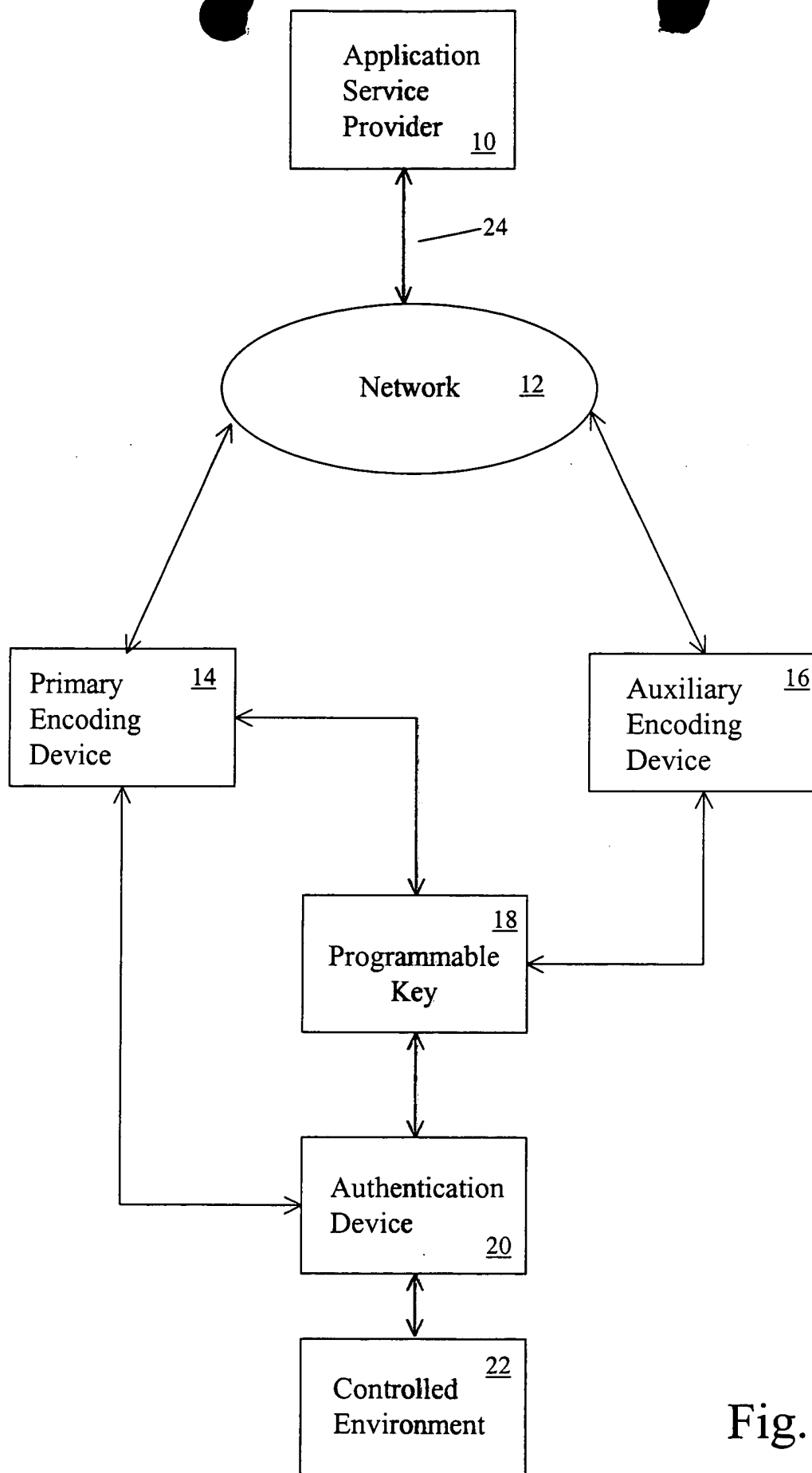
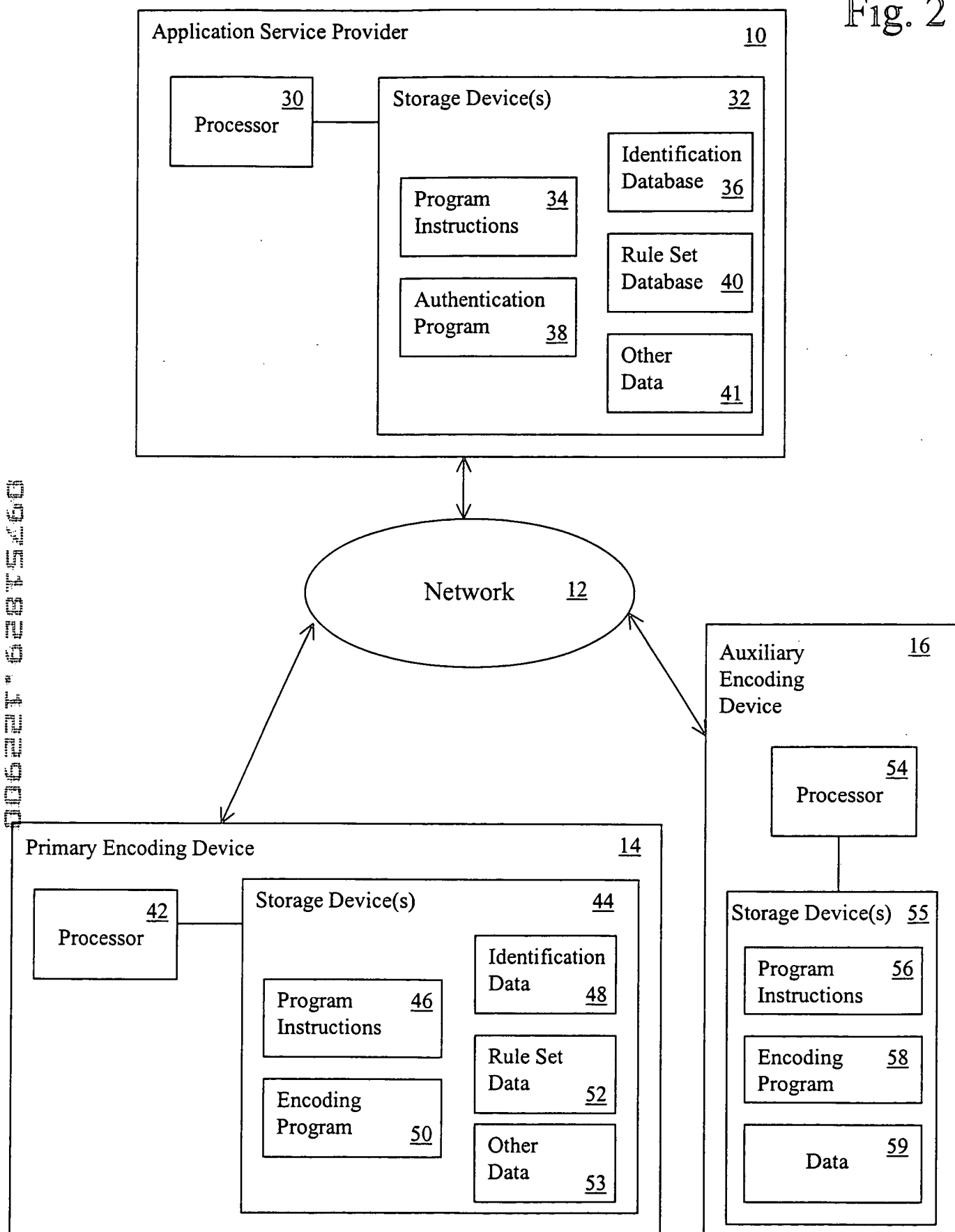


Fig. 1

Fig. 2



```

graph LR
    P60[Processor 60] --- SD62[Storage Device(s) 62]
    subgraph SD62 [Storage Device(s) 62]
        P64[Program Instructions 64]
        ID70[Identification Data 70]
        AP66[Authentication Program 66]
        RSD72[Rule Set Data 72]
        OP68[Override Program 68]
        OD74[Other Data 74]
    end
  
```

The diagram illustrates the internal components of an Authentication Device. A central Processor (60) is connected to a Storage Device(s) (62). The storage device contains six distinct data elements: Program Instructions (64), Identification Data (70), Authentication Program (66), Rule Set Data (72), Override Program (68), and Other Data (74).

```
graph LR; A[Key Disabling Logic 78] --- B[Storage Device(s) 80]; B --- C[Identification Data 82]; B --- D[Other Data 84];
```

The diagram illustrates the components of a Programmable Key system. A central box labeled "Key Disabling Logic" (78) is connected to a larger box labeled "Storage Device(s)" (80). Inside the "Storage Device(s)" box, there are two sub-components: "Identification Data" (82) and "Other Data" (84).

Fig. 4

09751829-122900

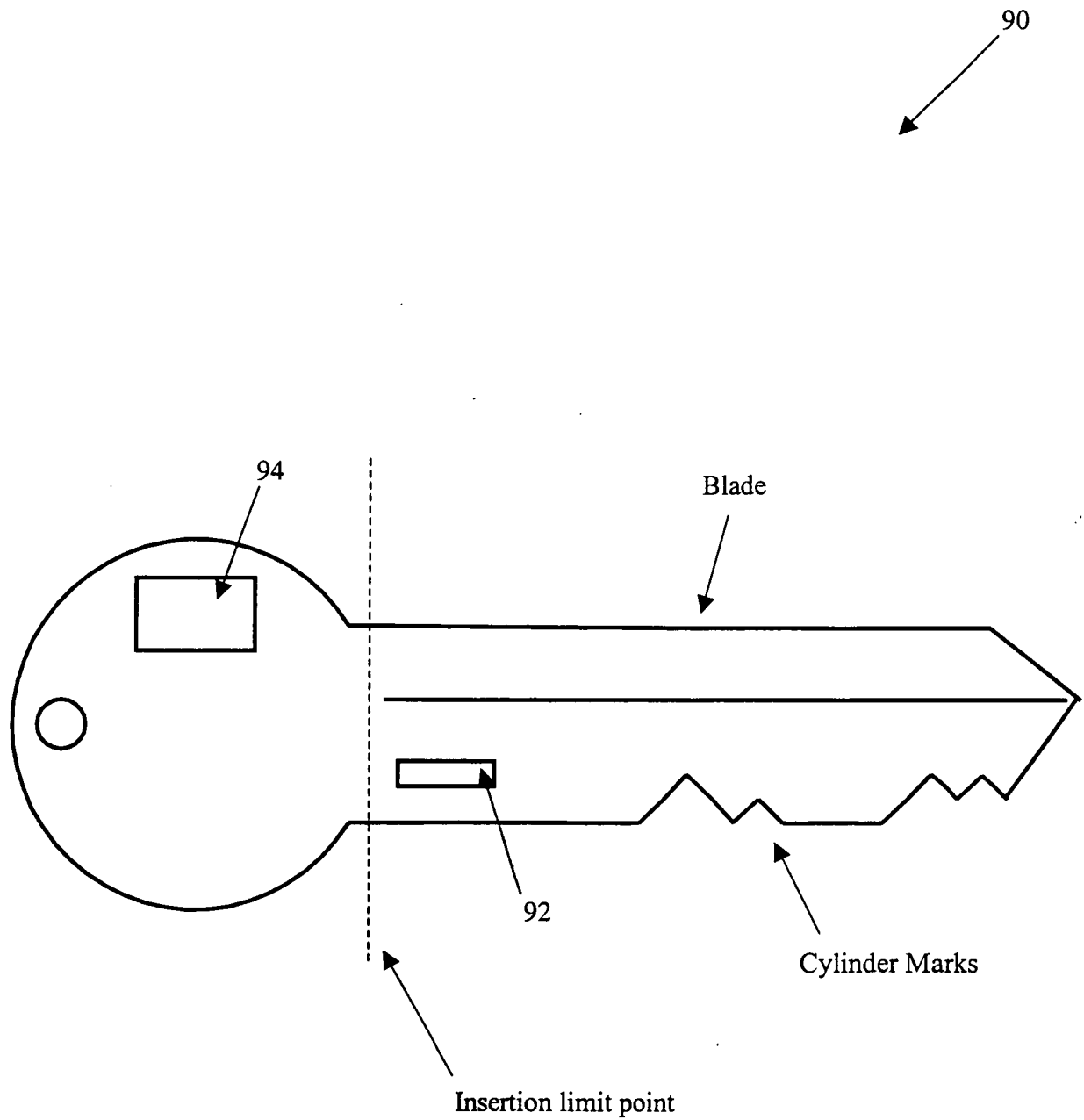


Fig. 5

09751829.122000

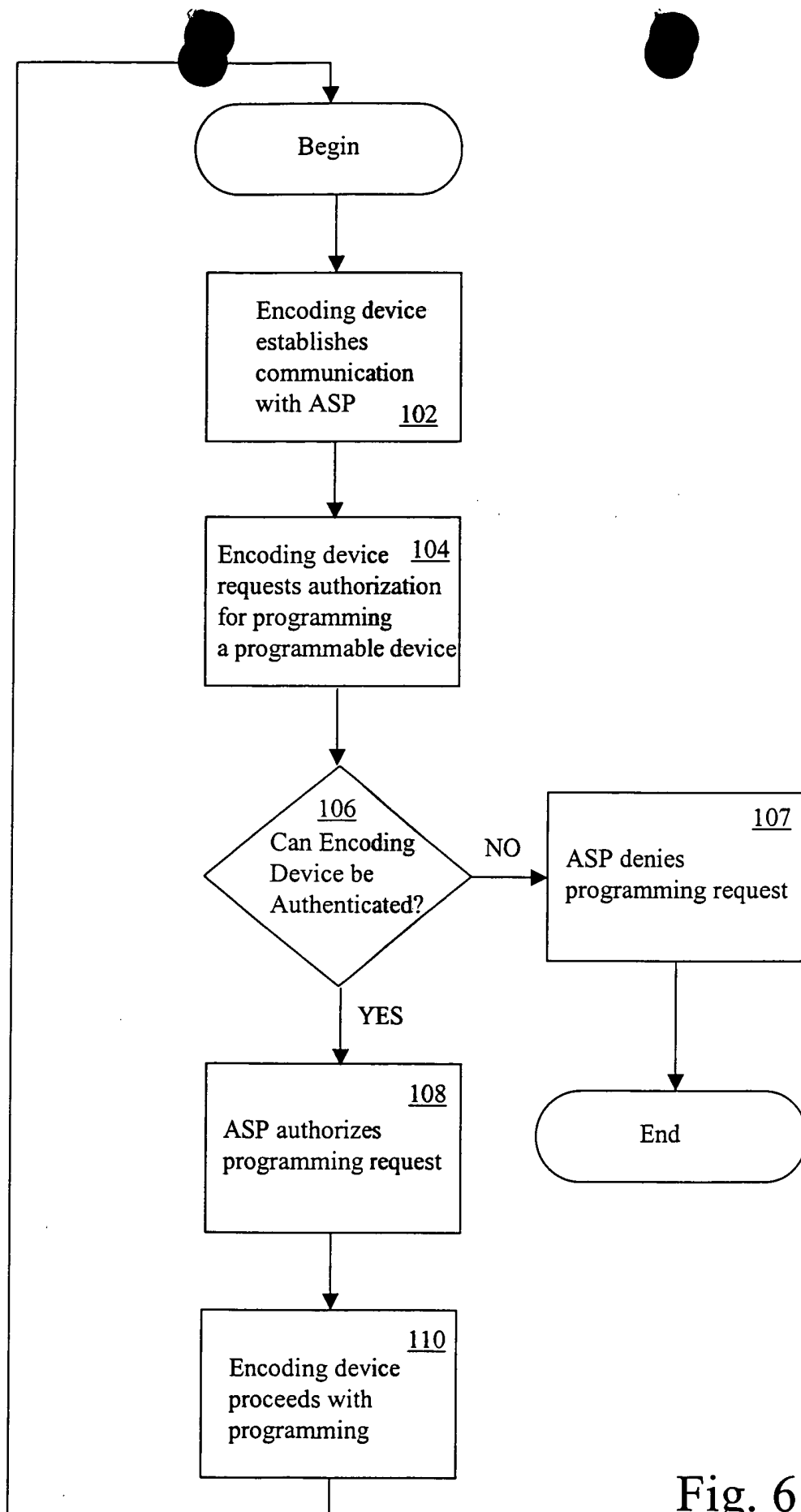


Fig. 6

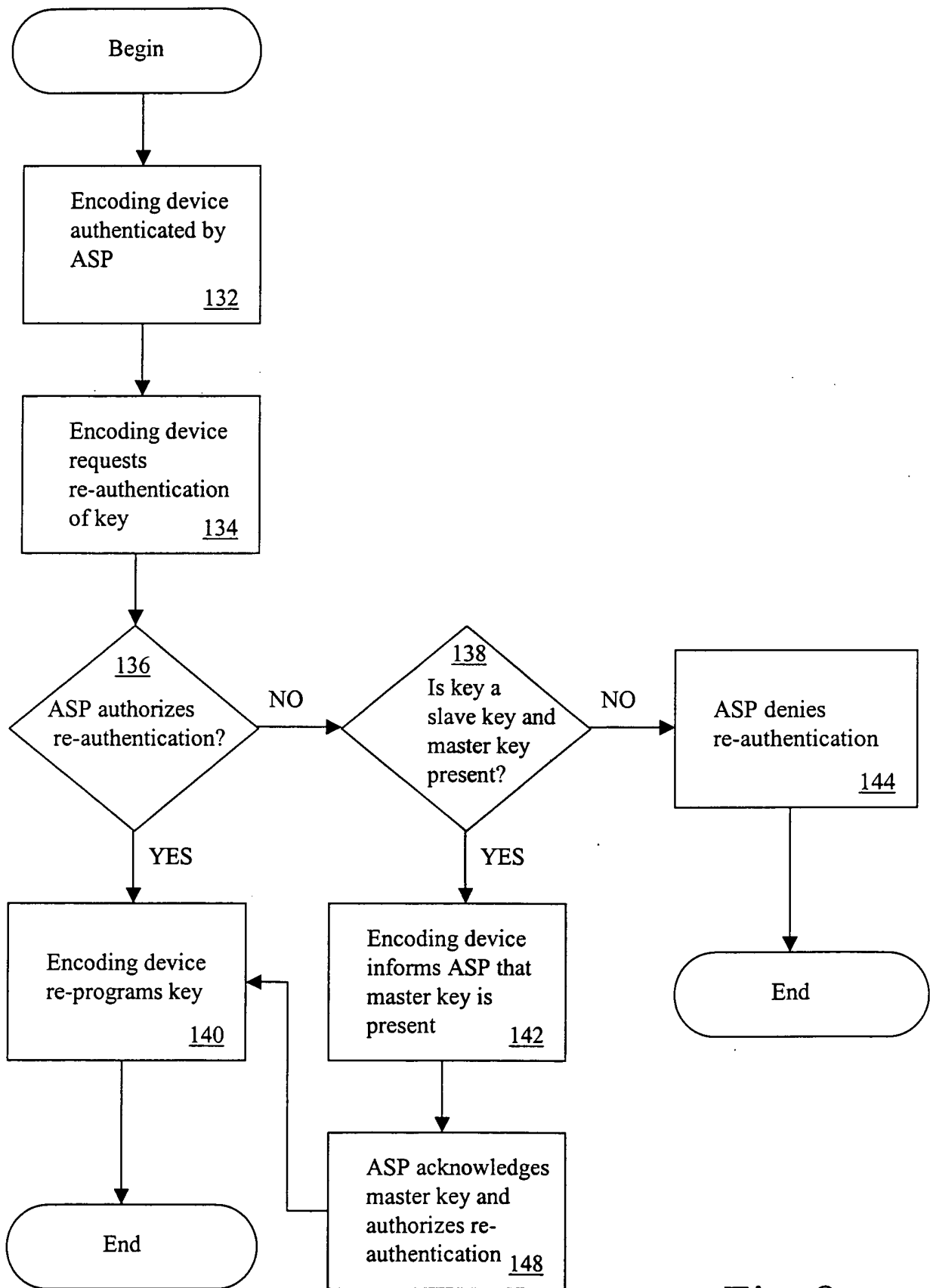


Fig. 8